

**WHAT IS CLAIMED IS:**

1. A multi-domain liquid crystal display device comprising:
  - a first substrate defined by a plurality of pixel regions;
  - a common auxiliary electrode around each pixel region on the first substrate;
  - a dielectric structure on a second substrate, the dielectric structure being divided each pixel region into a plurality of domains;
  - an additional structure compensating electric field around a corner portion within each pixel region; and
  - a liquid crystal layer between the first substrate and the second substrate.
2. The multi-domain liquid crystal display device of claim 1, further comprising:
  - a plurality of gate lines on the first substrate in a first direction;
  - a plurality of data lines formed in a second direction to cross the first direction;
  - thin film transistors formed in a portion where the gate lines cross the data lines; and
  - a pixel electrode connected with the thin film transistors in the pixel region defined by the gate lines and the data lines.
3. The multi-domain liquid crystal display device of claim 1, further comprising an alignment film on at least one of the first substrate and the second substrate.
4. The multi-domain liquid crystal display device of claim 1, wherein the common

auxiliary electrode is formed with gate lines.

5. The multi-domain liquid crystal display device of claim 1, wherein the common auxiliary electrode is not formed with the gate lines.

6. The multi-domain liquid crystal display device of claim 1, wherein liquid crystal molecules within each domain have different alignment directions.

7. The multi-domain liquid crystal display device of claim 1, wherein the dielectric structure includes a first region formed in one direction within the pixel region, and second and third regions respectively divided from an end portion of the first region.

8. The multi-domain liquid crystal display device of claim 1, wherein the dielectric structure includes an electric field induction window.

9. The multi-domain liquid crystal display device of claim 8, wherein the electric field induction window includes a hole or slit.

10. The multi-domain liquid crystal display device of claim 1, wherein the additional structure is at least one electric field induction window or dielectric structure.

11. The multi-domain liquid crystal display device of claim 1, further comprising a phase difference film on at least one of the first and second substrates.

12. A multi-domain liquid crystal display device comprising:

first and second substrates;

a plurality of gate lines on the first substrate in a first direction;

a plurality of data lines formed in a second direction to cross the first direction;

a plurality of thin film transistors formed in a portion where the gate lines cross the data lines;

a plurality of pixel regions between neighboring gate and data lines;

a common auxiliary electrode around each pixel region;

a plurality of pixel electrodes formed in each pixel region to connect with the thin film transistors;

a dielectric structure on the second substrate, the dielectric structure being applied electric field with the common auxiliary electrode and being divided the pixel region into at least four domains;

at least one or more additional structures formed at an end portion of the dielectric structure within the pixel region;

an alignment film on at least one of the first substrate and the second substrate; and

a liquid crystal layer between the first substrate and the second substrate.

13. The multi-domain liquid crystal display device of claim 12, further comprising a phase difference film on at least one of the first and second substrates.

14. A multi-domain liquid crystal display device comprising:  
first and second substrates;  
a plurality of gate lines and data lines on the first substrate to cross each other;  
common auxiliary electrodes within and around each pixel region on the first substrate;  
a plurality of dielectric structures on the second substrate corresponding to a region where the common auxiliary electrodes are not formed; and  
a liquid crystal layer between the first substrate and the second substrate.

15. The multi-domain liquid crystal display device of claim 14, wherein each pixel region is divided into at least three domains.

16. The multi-domain liquid crystal display device of claim 14, wherein the dielectric structures have a zig-zag shape.

17. The multi-domain liquid crystal display device of claim 14, wherein the common auxiliary electrodes have an extension portion electrically connected with the common auxiliary electrode of a neighboring pixel region.

18. The multi-domain liquid crystal display device of claim 14, further comprising an alignment film on at least one of the first and second substrates.

19. The multi-domain liquid crystal display device of claim 14, further comprising a phase difference film on at least one of the first and second substrates.

20. A multi-domain liquid crystal display device comprising:

- first and second substrates;
- a plurality of gate lines on the first substrate in a first direction;
- a plurality of data lines formed in a second direction to cross the first direction;
- a plurality of thin film transistors formed in a portion where the gate lines cross the data lines;
- a plurality of pixel regions between neighboring gate and data lines;
- a common auxiliary electrode around and within each pixel region;
- a plurality of pixel electrodes formed in each pixel region to connect with the thin film transistors;
- a plurality of dielectric structures on the second substrate in a region where the common auxiliary electrode is not formed;
- an alignment film on at least one of the first and second substrates; and
- a liquid crystal layer between the first and second substrates.

21. The multi-domain liquid crystal display device of claim 20, further comprising a phase difference film on at least one of the first and second substrates.

21. The multi-domain liquid crystal display device of claim 20, further comprising a phase difference film on at least one of the first and second substrates.